

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	239	(712/24).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:37
S2	0	SIMD near4 (bus\$2 near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 14:06
S3	3	SIMD with (bus\$2 near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:40
S4	8	SIMD same (bus\$2 near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:45
S5	0	SIMD near4 (co?process\$3 near4 bus\$2)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:48
S6	28	SIMD same (bus\$2 near4 size)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:46
S7	0	SIMD with (co?process\$3 near4 bus\$2)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:52
S8	0	SIMD with (co?process\$3 with bus\$2)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:52
S9	9	SIMD same (co?process\$3 with bus\$2)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 10:52
S10	3	((("6353863") or ("6192073") or ("5029073"))).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 11:24
S11	9	SIMD near4 (bus\$2 near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 14:44

S12	0	(CPU or (central adj1 process\$3 adj1 unit\$1)) same (decod\$3) same (ALU or (arithmetic adj1 logic)) same memor\$3 same (control adj1 signal\$1) same bus\$2 same (SIMD or (single adj1 instruction adj1 multiple adj1 data))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 16:21
S13	0	(decod\$3) same (ALU or (arithmetic adj1 logic)) same memor\$3 same (control adj1 signal\$1) same bus\$2 same (SIMD or (single adj1 instruction adj1 multiple adj1 data))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 16:22
S14	86	(decod\$3) same (ALU or (arithmetic adj1 logic) or execut\$3 or process\$3) same memor\$3 same bus\$2 same (SIMD or (single adj1 instruction adj1 multiple adj1 data))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 16:31
S15	27	(decod\$3) same (ALU or (arithmetic adj1 logic) or execut\$3 or process\$3) same memor\$3 same bus\$2 same (control adj1 signal\$1) same (SIMD or (single adj1 instruction adj1 multiple adj1 data))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 16:26
S16	14	(CPU or (central adj1 process\$1)) same (decod\$3) same (ALU or (arithmetic adj1 logic) or execut\$3 or process\$3) same memor\$3 same bus\$2 same (SIMD or (single adj1 instruction adj1 multiple adj1 data))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/03/09 16:31